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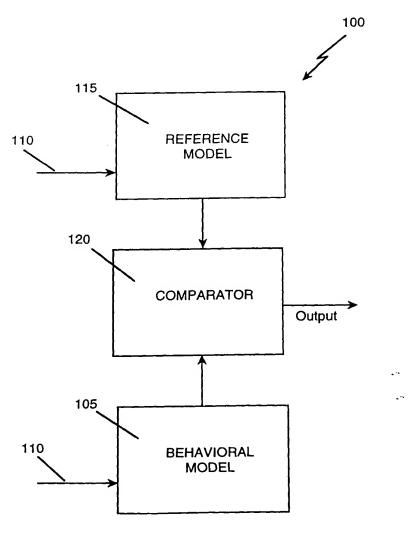
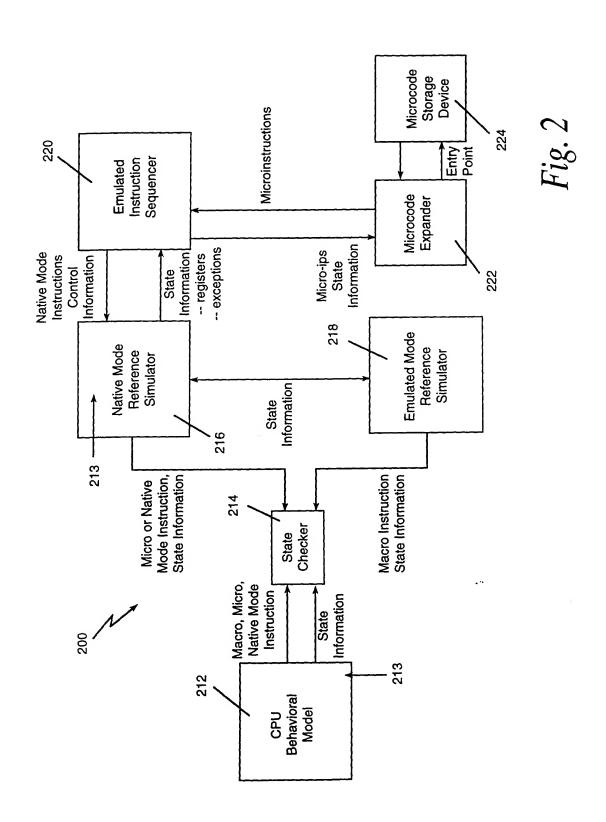


Fig. 1

Title. METHOD AND APPARATUS FOR VERIFYING THE CORRECTNESS OF A PROCESSOR BEHAVIORAL MODEL
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